Task No 01:

Construct the circuits of the following using multiplexer on Multisim along with the truth tables:

1. 4 variable expressions:

F(A,B,C,D)= Σ (1,3,4,11,12,13,14,15)

F(A,B,C,D)= π (1,3,4,11,12,13,14,15)

1. full adder design
2. basic ALU Design (AND, OR, NAND, NOR)

Solution:

Output: